

Dr. Samuel George Institute of Engineering & Technology

Approved By AICTE, New Delhi & Affiliated to JNTUK, KAKINADA.
An ISO 9000 : 2001 Certified Institution



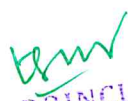
Phone : +918596-200064 (Off)
Mobile : 9618257287, 9849332122
Fax : +918596-223127

Website : www.drsgiet.ac.in
e-mail : sgit.principal@gmail.com
drsgit_35@yahoo.co.in

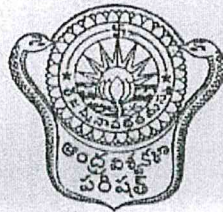
College Code : 35

2.4.2 (5). LIST OF FULL TIME TEACHERS WITH Ph.D (A.Y 2018-19)

SL.NO	NAME OF THE TEACHER	QUALIFICATION	DESIGNATION	NAME OF THE DEPARTMENT	DATE OF JOINING
1	DR.Y.ESWARA RAO	Ph.D	PRINCIPAL	CIVIL ENGINEERING,	15-05-2014
2	DR.C.RAJALINGAM	Ph.D	PROFESSOR	ELECTRICAL AND ELECTRONICS ENGINEERING	02-06-2017
3	DR.A.K.S.ANSARI	Ph.D	PROFESSOR	MECHANICAL ENGINEERING	02-06-2017
4	DR S.VASANTHA SWAMINATHAN	Ph.D	PROFESSOR	ELECTRONICS AND COMMUNICATIONS ENGINEERING	02-06-2017
5	DR.P.PEDDA SADHU NAIK	Ph.D	PROFESSOR	COMPUTER SCIENCE AND ENGINEERING	13-10-2004
6	DR.V.BALAJI	Ph.D	PROFESSOR	POWER ELECTRONICS	01-06-2017
7	DR P.MURUGESAN	Ph.D	PROFESSOR	THERMAL ENGINEERING	05-06-2017
8	DR.G.SUDHAGAR	Ph.D	PROFESSOR	DIGITAL ELECTRONICS AND COMMUNICATION ENGINEERING	05-06-2017
9	DR.P.PRABHAKARAN	Ph.D	PROFESSOR	COMPUTER SCIENCE AND ENGINEERING	06-06-2017


PRINCIPAL
Dr. SAMUEL GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
MARKAPUR-523 316, Prakasam Dist.

ANDHRA UNIVERSITY



FACULTY OF ENGINEERING

This is to certify that

Yallamati Eswara Rao

has been duly admitted to the

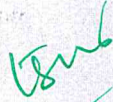
Degree of Doctor of Philosophy

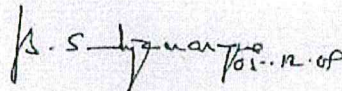
*in this University, he having been declared to be
qualified to receive the same in 2008.*

Given under the Seal of the University

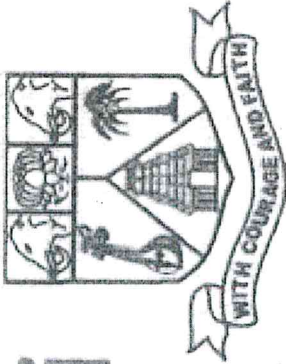
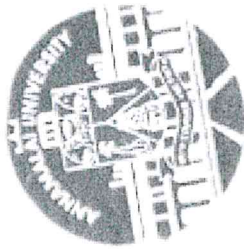
VISAKHAPATNAM

5th December, 2009


PRINCIPAL
Dr. SAMUEL GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
MARKAPUR-523 316, Prakasam Dist.


Vice-Chancellor

Annamalai



University

Register No. : 2013104329

PROVISIONAL CERTIFICATE

This is to certify that **RAJALINGAM.C**
has passed the

Ph.D IN ELECTRICAL AND ELECTRONICS ENGINEERING

Examination held in **21.03.2017**

and that he/she was

**THIS TITLE: DESIGN OF HYBRID EMBEDDED CONTROLLER TO REDUCE
THE TORQUE RIPPLE IN A BRUSHLESS DC MOTOR DRIVE**

He/she has qualified himself / herself for the **Ph.D IN ELECTRICAL AND ELECTRONICS
ENGINEERINGG**

Annamalai Nagar - 608002

Date : **03/07/2017**



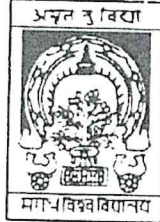
Assistant Registrar (Examination)

PRINCIPAL
Dr. SAMUEL GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
MARKAPUR-523 316,Prakasam Dist

Registration No. 18/31/2007

9506018 S.D.M. + M.O. + A.P. +
N. K. +
18/31/2007

Magadh University



This is to certify that V. Raju

Who having submitted his / her research thesis on "Study And Analysis Of Model Predictive Control Of PH In Pharmaceutical Process"

for the degree of **Doctor of Philosophy** for examination in the faculty of Engineering (Electrical And Electronic), held in the month of June '2010 was admitted this day to the degree.

MAGADH UNIVERSITY, BODH-GAYA

Date 23rd November 2010

C. S. Reddy
Vice-Chancellor

Dr. Samuel George
PRINCIPAL

Dr. SAMUEL GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
MARKAPUR-523 316, Prakasam Dist

Reg No : 18227629841

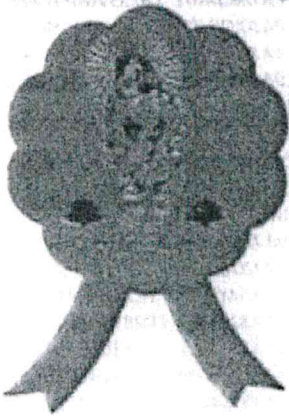
Sl. No. 3412804453

C.No: 4056

Vinayaka Missions University



The Board of management of the Vinayaka Missions University here by makes known that
A.K.S.ANSARI ALIAS KADARAMEERA
has been admitted to the degree of
Doctor of Philosophy in Mechanical Engineering
for The topic - **DESIGN AND SIMULATION OF EFFECTIVE COOLING IN VENTILATED BRAKE DISC**
who has successfully completed the prescribed course of Study
and is therefore awarded this
Doctorate Degree
with all the honours, Right Privileges to that degree pertaining
Given on this 29th day of August in the year two-thousand sixteen in the State of Tamilnadu



Given Under the seal of the university
under section 3 of the UGC Act 1956
(Formerly Vinayaka Missions Research Foundation Deemed University)




Registrar


Vice-Chancellor

Date :
29/08/2016
Salem, India




PRINCIPAL
Dr. SAMUEL GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
MARKAPUR-523 316,Prakasam Dist

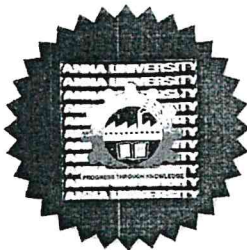


Reg. No.:2003279708/RG

The Syndicate of the Anna University Chennai hereby makes known that **MURUGESAN P** has been admitted to the **DEGREE OF DOCTOR OF PHILOSOPHY** under the **Faculty of Mechanical Engineering**, having been certified by the duly appointed examiners to be qualified to receive the same in the year **2009**.

Title of the thesis:

ANALYSIS AND SELECTION OF THIRD PARTY REVERSE LOGISTICS PROVIDER



Given under the Seal of the University

Chennai 600 025
India
February 2010
C 54012007

V. My...
Controller of Examinations

[Signature]
Registrar

[Signature]
Vice-Chancellor

[Signature]
PRINCIPAL
Dr. SAMUEL GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
MARKAPUR-523 316,Prakasam Dist.



அண்ணாமலைப் பல்கலைக்கழகம்
ANNAMALAI UNIVERSITY

Reg. No. : 005X02T13174



பொறியியற்புலம்
FACULTY OF ENGINEERING & TECHNOLOGY

ஜூலை 2016 இல் மின்னணு மற்றும் தொடர்பியல் பிரிவில் நடத்திய தேர்வுகளில் வசந்த ஸ்வாமிநாதன். ச தேர்ச்சியடைந்து ஆய்வேட்டை மதிப்பிட முறையாக அமைக்கப்பெற்ற தேர்வுக்குழுவினர்சான்றளித்தபடி ஆய்வியல் அறிஞர் பட்டம் பெறுதற்கு உரியவர் ஆகின்றார் என அண்ணாமலைப் பல்கலைக்கழக ஆட்சிக்குழு இதன்வழி அறிவிக்கின்றது

The Syndicate of the Annamalai University hereby makes known that VASANTHA SWAMINATHAN S has been admitted to the Degree of DOCTOR OF PHILOSOPHY in ELECTRONICS AND COMMUNICATION ENGINEERING he/she having been certified by duly appointed examiners Who Valued his/her thesis to be qualified to receive the same in JULY 2016 .

TITLE OF THE THESIS

NET FPGA IMPLEMENTATION OF ULTRA-LOW LATENCY TCP/IP FOR FINANCIAL TRADING SYSTEMS

Given under the seal of the University



Samuel George
PRINCIPAL
Dr. SAMUEL GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
MARKAPUR-523 316,Prakasam Dist.
Registrar

Dr. Jeyapalan
Vice-Chancellor

Annamalai Nagar - 608002
Dated :- 06/03/2017

Prakasam
Deputy Controller of Examinations
(Academic)
துணை தேர்வாணையர் (கல்வி சார்ந்த)

பதிவாளர்

துணைவேந்தர்

उपाधि क्रमांक/Degree Serial No.
Ph.D/543998

नामांकन संख्या /Enrollment No.
201173367362

अनुक्रमांक संख्या /Roll No.
7542773337

गौतम बुद्ध प्राविधिक विश्वविद्यालय



(पूर्ववर्ती उत्तर प्रदेश प्राविधिक विश्वविद्यालय, लखनऊ)

विश्वविद्यालय की विद्या परिषद की अनुशंसा पर

जी. सुधागर

को इलेक्ट्रिकल इंजीनियरिंग (ऊर्जा इंजीनियरिंग) विद्या में वाचस्पति की विद्या उपाधि
प्रथम श्रेणी में

उमके द्वारा इस उपाधि की अवाप्ति हेतु

विश्वविद्यालय द्वारा विहित अपेक्षाओं को सफलतापूर्वक पूरा करने पर

एतद्वारा सन् 2014 में प्रदान की जाती है।

उक्त उपाधि के लिये आपने निम्न विषय पर गौतिक प्रबन्ध प्रस्तुत किया।

कार्यान्वयन और औद्योगिक अनुप्रयोगों के लिए दबाव गरम पानी का भंडारण का उपयोग सौर
ताप प्रणाली का डिजाइन।

Gautam Buddha Technical University

(Formerly Uttar Pradesh Technical University, Lucknow)

Upon the recommendation of the Academic Council,

the University hereby confers the degree of

Doctor of Philosophy Electrical Engineering (Energy Engineering)
upon

G.SUDHAGAR

*who has successfully completed the requirements prescribed by the University
for the award of this degree in*

First Division

in the year 2014

The subject on which he/she presented a Thesis for the Degree was:-
**Implementation And Design Of Solar Thermal Systems Utilising
Pressurized Hot Water Storage For Industrial Applications**

लखनऊ, (उ.प्र.), भारत
Lucknow, (U.P.), India

दिनांक: / Dated: December 31, 2014

(कृपा शंकर)
कुलपति
(Kripa Shanker)
Vice-Chancellor

PRINCIPAL
OF SAMUEL GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
MARKAPUR-523 316, Prakasam Dist.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
KAKINADA - 533 003, ANDHRA PRADESH, INDIA

College :JNTUK Kakinada

Hall Ticket No. 10022P0561

Sl. No. K00270254
PC. No. 201750095



PROVISIONAL CERTIFICATE

This is to certify that Mr/Mrs. **P PEDDA SADHU NAIK**


son/daughter of Shri. **P CHINNA MANTHRU NAIK** has satisfied all

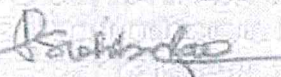
the requirements for the award of **Doctor of Philosophy** in the faculty

of **COMPUTER SCIENCE & ENGINEERING** for the Thesis entitled "Image

Segmentation using Non-Parametric Mean Shift Clustering Algorithms".

Date 26-05-2017


Controller of Examinations


Director of Evaluation


Registrar


PRINCIPAL

Dr. SAMUEL GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
MARKAPUR-523 316,Prakasam Dist



பொறியியற் புலம்
FACULTY OF ENGINEERING

மனோம்மனியம் சுந்தரனார் பல்கலைக்கழக ஆட்சிக்குழு
பிரபாகரன் ப

கணினி இயல் மற்றும் பொறியியல் பாடப்பிரிவில்

அறிஞர் பட்டம் பெறுவதற்குத் தகுதியுடையவர் என முறையாக அமைக்கப்பெற்ற
தேர்வினோர் சான்றளித்தவாறு டிசம்பர் 2012 ஆம் ஆண்டில்
இப் பட்டப்பேற்றுக்கு உரியவர் ஆகின்றார் என இதன்வழி அறிவிக்கின்றது.

பல்கலைக்கழக முத்திரையுடன் இது வழங்கப்படுகின்றது.

The Syndicate of the MANONMANIAM SUNDARANAR UNIVERSITY
hereby makes known that PRABAKARAN P
has been admitted to the Degree of Doctor of Philosophy in
COMPUTER SCIENCE AND ENGINEERING

he/she having been certified by duly appointed Examiners to be qualified
to receive the same in the year DECEMBER 2012



Given under the seal of the University.

திருநெல்வேலி, இந்தியா
Tirunelveli, India

நாள், Dated 11.02.2013

PRINCIPAL
Subashree GEORGE INSTITUTE
OF ENGINEERING & TECHNOLOGY
(Dr. P. Govindaraju)
தேர்வுகணையர் புதிவாளர்
KANKAR, OR-523 316, Prakasam Dist

Controller of Examinations

Registrar

(Dr. A.K. Kumaraguru)

துணை வேந்தர்

Vice-Chancellor